

Electronic Acknowledgement Receipt

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Title of Invention:	RISC MICROPROCESSOR ARCHITECTURE IMPLEMENTING MULTIPLE TYPED REGISTER SETS
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		20470600003poa373.pdf	161615	yes	3
	Multipart Description/PDF files in .zip description				
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	Assignee showing of ownership per 37 CFR 3.73(b).		2	3	
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